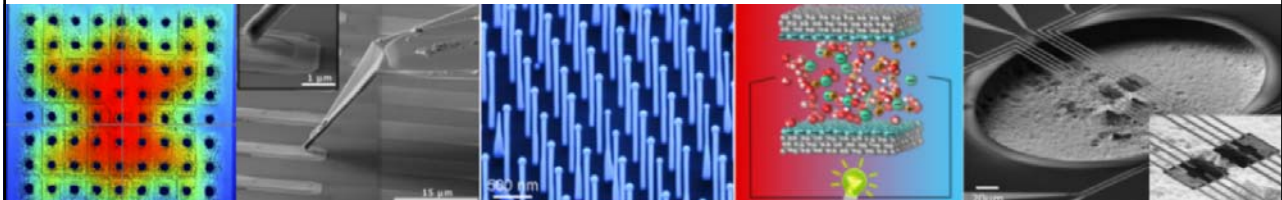


Journées du GDR-NAME 19-21 mai 2021



# Atelier #6

## Micro-Nano-Devices (for energy applications)



## Outline

### 1. Thermo-Photo devices

- Integration of Thermoelectric micro-nano devices
- Photovoltaics devices
- Thermophotovoltaic devices (with nanoscale gaps)

### 2. Kinetic devices

- Piezoelectric Kinetic Energy Harvesters
- Electrostatic Kinetic Energy Harvesters

### 3. Other devices

- Nano LED
- Adiabatic logic with micro/nano devices
- Piezoelectric energy converters

### 4. Metrology

- Metrology for thermal properties of thermoelectric micro/nano devices
- Metrology & standardization for kinetic energy harvesters

# Integration of Thermoelectric micro-nano devices

Dimitri Tainoff (Inst. Neel, MOIZ)  
Katir Ziouche (IEMN)

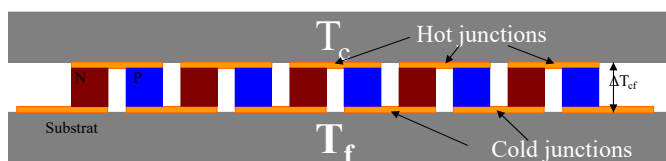
- Integration of Thermoelectric micro-nano devices
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- Adiabatic logic with micro/nano devices
- Piezoelectric energy converters
- Metrology for thermal properties of thermoelectric micro/nano devices
- Metrology & Standardization for kinetic energy harvesters

Working Group 6 : Micro Nano Devices



## Vertical technology

Bulk → micro devices



*Thermocouples perpendicular to both surfaces*

- Most widespread technology, → miniaturization is challenging.
- The heat flows directly through the thermoelectric materials → The device efficiency depends only on thermoelectric materials

→ **Figure of merit improvement ZT**

⇒ Maximise the power factor  $\alpha^2 \cdot \sigma$

⇒ Minimise the thermal conductivity  $\kappa$

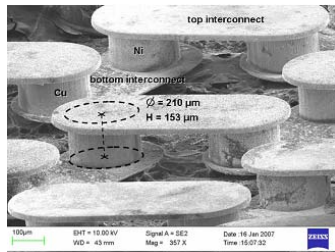
Materials  
and ZT  
# group 3

Working Group 6 : Micro Nano Devices

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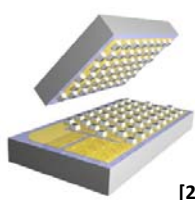


## Examples of vertical technology integration

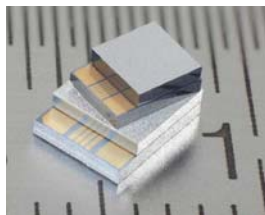


- Made on flexible substrate
- Thermoelectric microgenerator ( $\mu$ TEG) and thermal microfluxmeter
- GreenTEG company created from Swiss Federal Institute of Technology (ETH)

[1]



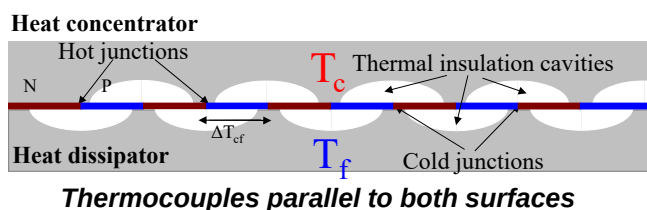
[2]



- Vertical technology of  $\mu$ TEG or fluxmeter (marketed products, MPG-D751 made by "micropelt")
- Sensitivity of de 140 mV/K at 23° C (dimension 14 mm<sup>2</sup>, electrical resistance 300 $\Omega$ )

## Planar technology

### Micro-nano devices



- Easier to integrate onto nano/micro TE device fabricated on silicon
- Heat flows not only through TE materials (support, concentrator and dissipator)

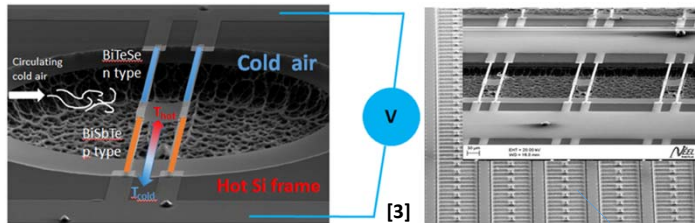
### Efficiency improvement :

- ⇒ Maximise the **figure of merit  $ZT$**  of materials
- ⇒ **Design optimization to collect heat and limit losses**

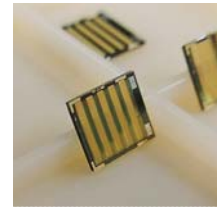
Materials and  $ZT$   
# group 3

# group 6

## Examples of planar technology integration (*micro-nano devices*)



- Planar technology
- Numerous nanoTEG are coupling in parallel → low electrical resistance
- Application : micro-source of energy for autonomous connected sensors.



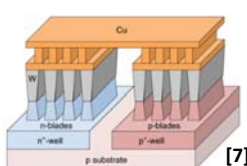
*Suspended thermopile  
(onto multi membranes)*



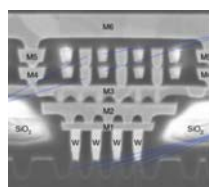
*Planar monolithic  
 $\mu$ TEG*

- Technology of  $\mu$ TEG
- High thermal planar resistance
- Out power  $\sim 300 \mu\text{W}/\text{cm}^2$  for 1 W injected

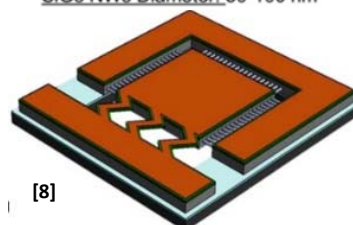
## Others examples of TE micro-nano devices



- Si integrated TE generators
- Vertical nanostructures

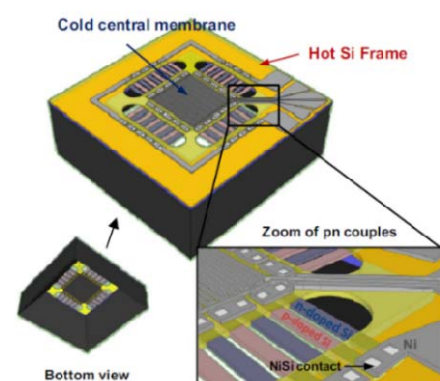


Single device area:  $2 \text{ mm}^2$   
SiGe NWs Length:  $10 \mu\text{m}$   
SiGe NWs Diameter:  $50\text{-}100 \text{ nm}$



[8]

- Top down micromachining
- Growth of SiGe nanowires



[5,6]

- Micropower TE generator from thin Si membranes

# Photovoltaics devices

Jean-Paul Kleider (GeePs, FedPV)

- Integration of Thermoelectric micro-nano devices
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- Piezoelectric Kinetic Energy Harvesters
- Electrostatic Kinetic Energy Harvesters
- Nano LED
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- Piezoelectric energy converters
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- Metrology & Standardization for kinetic energy harvesters

Working Group 6 : Micro Nano Devices



## Nano aspects: different levels, in different technologies

### • Nanostructured architectures

nanophotonics → increase light absorption through patterning

nanowires, radial junction nanowire solar cells

→ decouple light absorption and electrical transport/carrier extraction

### • Nanometric layers acting as passivation/buffer layers or enhancing collection of one type of carriers

→ concept of passivating selective contacts (e.g. in silicon PV)

→ transport/extraction layers in perovskite PV (e.g.  $\text{NiO}_x$  as hole transport layers,  $\text{TiO}_x$  as electron transport layers, self-assembled monolayers)

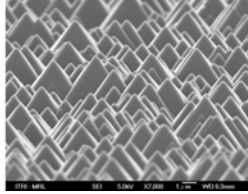
Working Group 6 : Micro Nano Devices

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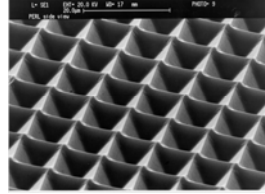


## Silicon nanowires: as an ultimate of texturing

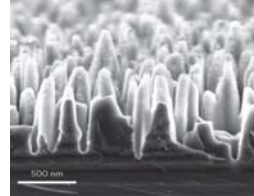
*Pyramid textured crystalline silicon [1]*



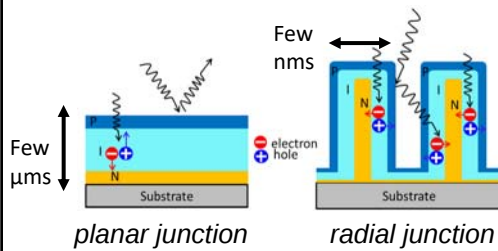
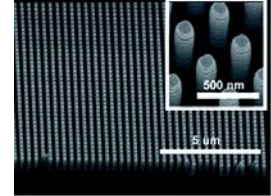
*Inverted pyramid silicon [2]*



*Black silicon [3]*



*Silicon nanowires [4]*

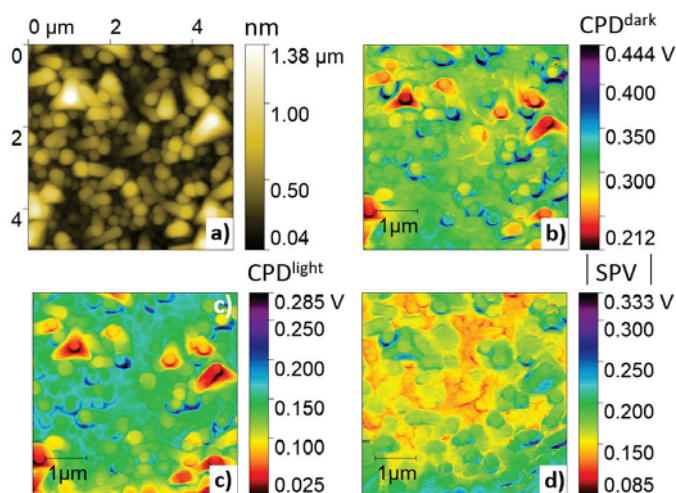


### Radial junction nanowire structures enable [5,6]

- Strong light trapping and anti-reflection
- Short distances to extract carriers
- Potentially higher open-circuit voltage and better performance for defective materials as compared to planar junction solar cells

**Main issues:** passivation of nanowires → at present, record efficiencies < 20%

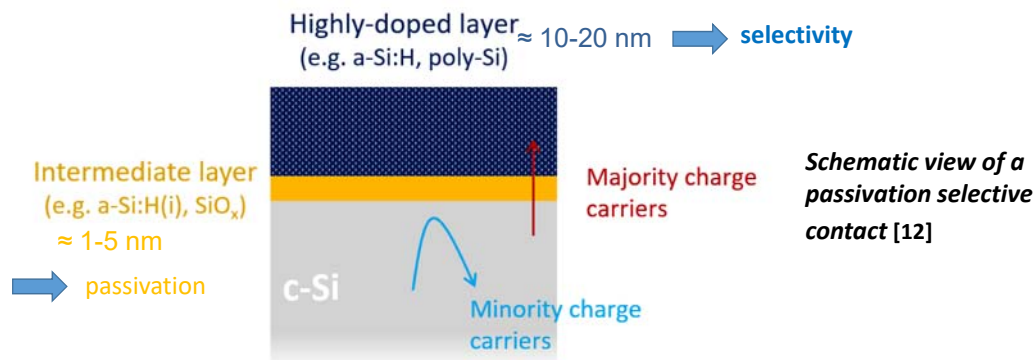
## Characterization of nanowire solar cells from AFM/C-AFM/KPFM



KPFM measurements performed at the same location on NWs [7] illustrating:

- the topography
- the contact potential difference (CPD) under dark conditions,
- the CPD under white LED illumination and
- the surface photovoltage (SPV) →  $V_{oc}$  of individual nanowires

## Concept of full area passivating selective contacts: example in Si



- **Silicon heterojunctions solar cells: (p)a-Si:H/(i)a-Si:H/c-Si**  
 $\rightarrow$  25.6% efficiency [8], world record of 26.7% in the IBC configuration [9]
- **Poly-Si based selective contacts: (p+)poly-Si/SiO<sub>x</sub>/c-Si**  
 $\rightarrow$  25.8% efficiency on 2x2 cm<sup>2</sup> area [10], 24.6% for 6"x6" [11]

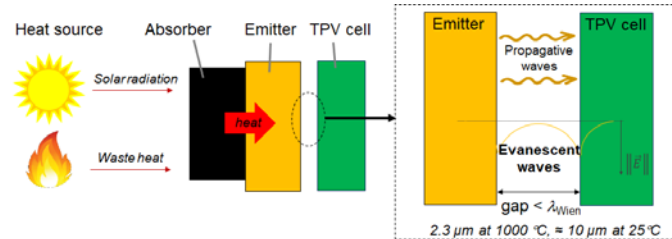
## Thermophotovoltaic devices with nanoscale gaps

Rodolphe Vaillon (IES)

- Integration of Thermoelectric micro-nano devices
- Photovoltaics devices
- Thermophotovoltaic devices (with nanoscale gaps)
- Piezoelectric Kinetic Energy Harvesters
- Electrostatic Kinetic Energy Harvesters
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## Thermophotovoltaic (TPV) devices with nanoscale gaps

- Principle:**

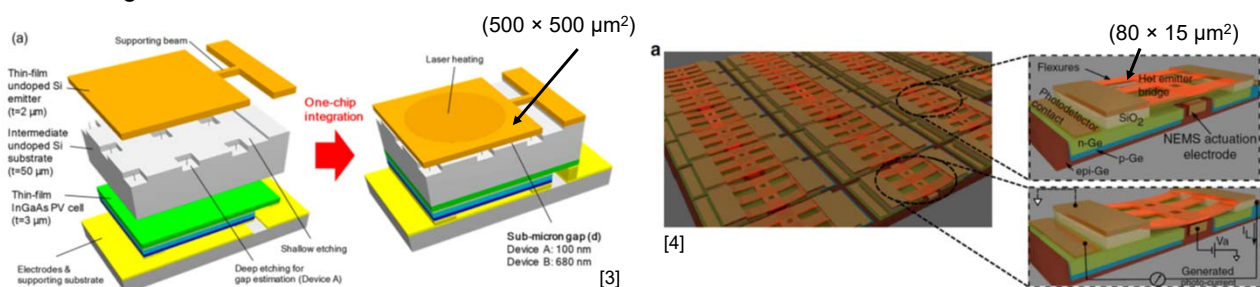


➡ Higher generated electrical power at nanoscale gaps

- Main parameters/materials and influence of nanomaterials:** structures and fabrication techniques for building a nanoscale ( $< 1 \mu\text{m}$ ) vacuum gap between the hot ( $> 500 \text{ }^\circ\text{C}$ ) and cold (room temp.) sides (see also near-field radiative heat transfer devices ([1] but  $\Delta T < 200 \text{ }^\circ\text{C}$ ))

## Recent first convincing experimental demonstrations [2-5]

- Among them, **two devices:**



- Performances:**  $< 1 \text{ mW.cm}^{-2}$ ; estimated efficiency  $< 1\%$
- Applications:** thermal energy harvesting. Research stage (TRL  $< 4$ )
- Future challenges:** devices with much larger areas, temperature differences and performances (emitter/cell design challenges: see Working Group 3)



## MEMS cantilever-type piezoelectric harvesters

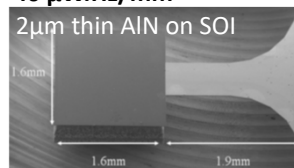
### • Key issues: for integration in MEMS technology

- ✓ Growth of oxide films on Si or metal substrates : inter-diffusion, large lattice & thermal expansion mismatch
- ✓ Clamping effect on the substrate  $\Rightarrow$  lower effective piezo coefficient
- ✓ Proper texturing of electrode layers
- ✓ If piezo layer thickness  $< 1\mu\text{m} \Rightarrow$  low generated voltage

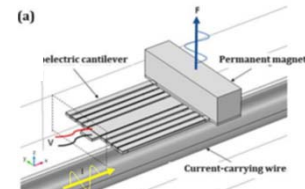
### • Performances:

$10^{-3}$  to  $10\ \mu\text{W}/(\text{cm}^2 \cdot \text{Hz} \cdot \text{g}^2)$   
 or  $10^{-4}$  to  $10^{-1}\ \mu\text{W}/(\text{mm}^3 \cdot \text{Hz} \cdot \text{g}^2)$   
 on a frequency range: 100-1000Hz [3]

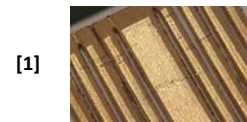
$0.5\mu\text{W}$ , 212Hz, 0.68g [2]  
 $40\ \mu\text{W} \cdot \text{Hz}/\text{mm}^3$



$9\mu\text{W}$ , 60Hz,  $3\ \mu\text{W} \cdot \text{Hz}/\text{mm}^3$   
 (depends on current and distance to wire)



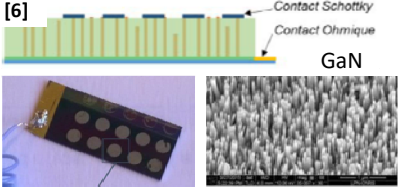
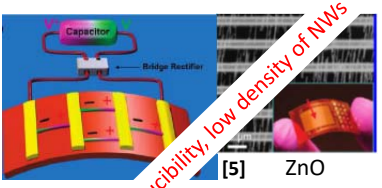
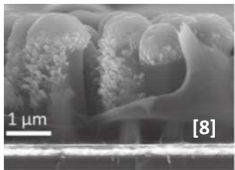
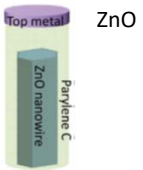
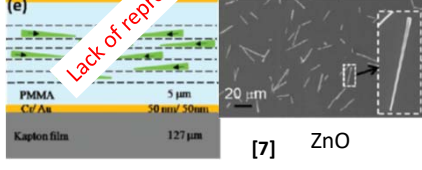
50 $\mu\text{m}$  thick PZT by screen printing on stainless steel



Gold ball-bonding connection

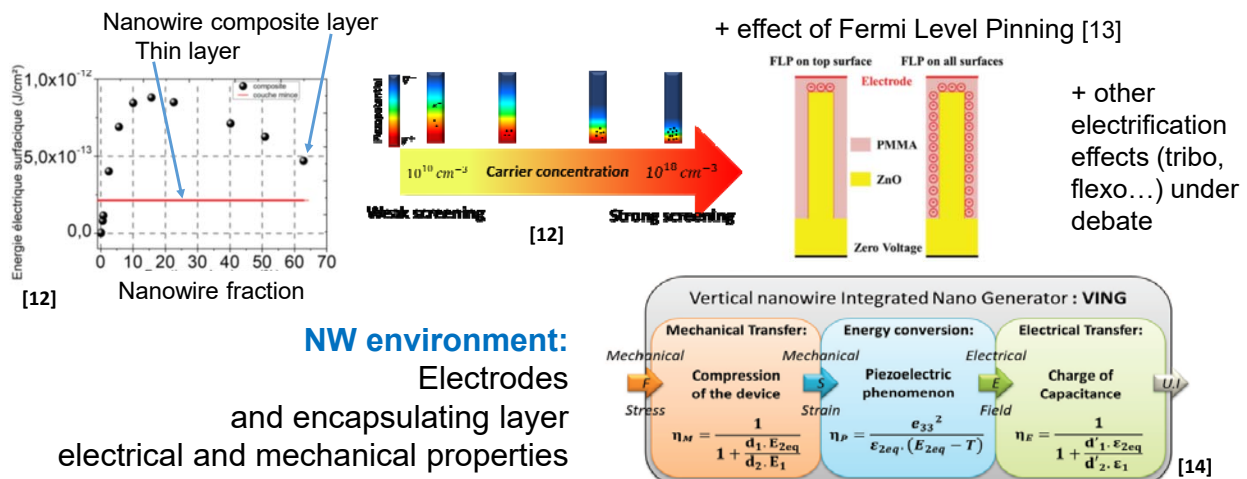
## Piezo-semiconducting nanowire based nanogenerators

A **NW array** embedded in a polymer matrix, between electrodes and on a rigid or flexible substrate. Various configurations :

|                                                                    | NWs orthogonal to the substrate                                                                                                                                                                                                                       | NWs parallel to the substrate                                                                                                                                                                                                     |
|--------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Electrodes are in contact with the NWs                             | <p>[6]</p>  <p>Contact Schottky<br/>Contact Ohmique<br/>GaN</p>                                                                                                    |  <p>[5] ZnO</p>                                                                                                                               |
| Capacitive coupling between NWs and electrode via insulating layer |  <p>1 <math>\mu\text{m}</math> [8]</p>  <p>Top metal<br/>ZnO<br/>Paraffin C</p> | <p>(e)</p>  <p>PMMA<br/>Cr/Au<br/>Kapton film<br/>5 <math>\mu\text{m}</math><br/>50 nm/50 nm<br/>127 <math>\mu\text{m}</math><br/>[7] ZnO</p> |

## Main parameters / influence of nanostructured device

**NW should have [9,10,11]:** high aspect ratio, optimal density, low doping (charges screening), polarity uniformity.

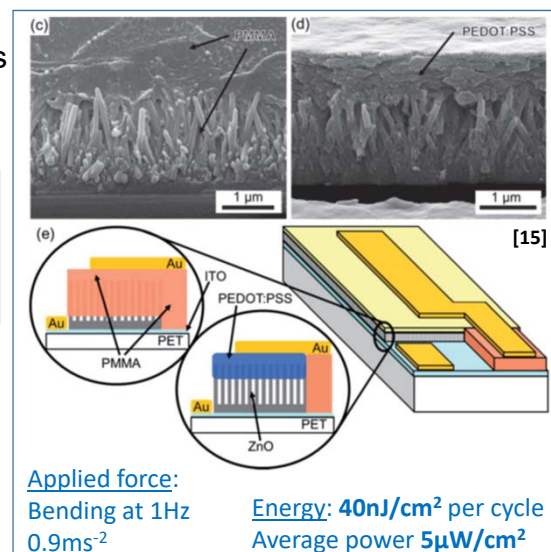
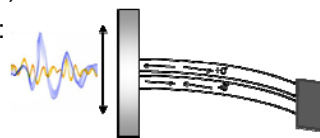


Working Group 6 : Micro Nano Devices

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## Performances and applications of nanogenerators

- Large variety of mechanical testing conditions (compression, bending) [15]
- Mostly under compression:  
 $1-10 \mu\text{W}/\text{cm}^2$  @ a few N force  
 @ 0.3 to 5 Hz [16]
- Comparison with bending cantilevers:  
 $10^{-3}$  to  $10 \mu\text{W}/(\text{cm}^2 \cdot \text{Hz} \cdot \text{g}^2)$   
 (base of a machine tool :  
 $10 \text{ m/s}^2$  [3]  $\approx 1\text{g}$ )



Working Group 6 : Micro Nano Devices

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# Electrostatic Kinetic Energy Harvesters

Philippe Basset (ESYCOM)

Alain Sylvestre (G2ELAB)

Elie Lefeuvre (C2N)

Dimitri Galayko (LIP6)

- Integration of Thermoelectric micro-nano devices
- Photovoltaics devices
- Thermophotovoltaic devices (with nanoscale gaps)
- Piezoelectric Kinetic Energy Harvesters
- **Electrostatic Kinetic Energy Harvesters**
- Nano LED
- Adiabatic logic with micro/nano devices
- Piezoelectric energy converters
- Metrology for thermal properties of thermoelectric micro/nano devices
- Metrology & Standardization for kinetic energy harvesters

Working Group 6 : Micro Nano Devices



## Main requirements for electrostatic transducers

→ Need (high) capacitance variations + polarization

- Spring-mass system (inertial or direct-pressure)
- Soft polymer covered by electrodes (direct-pressure)

- External DC source
- Charged electret
  - ✓ Injection
  - ✓ Poling
  - ✓ Tribo-electrification

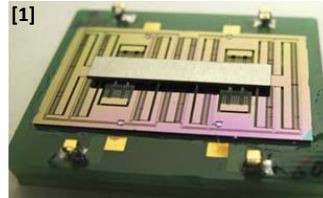
Working Group 6 : Micro Nano Devices

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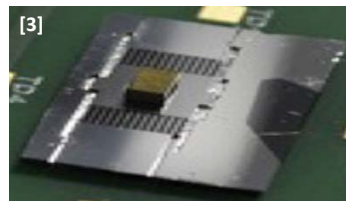


## Inertial Si-based MEMS KEH

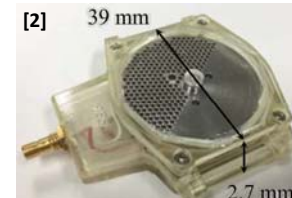
- External mass for low frequency / low  $g$  applications
- Bias from
  - ✓ External DC source
  - ✓ Corona discharge
  - ✓ "Solid-state" electret
- Typ.  $1\text{--}10\ \mu\text{J}$  / mechanical cycle /  $g$  /  $\text{cm}^2$



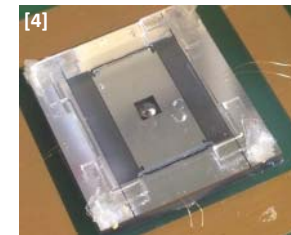
For SHM (50 Hz)



For pacemaker (10-20 Hz)



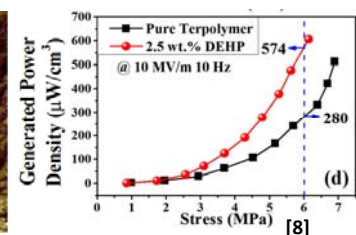
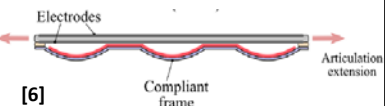
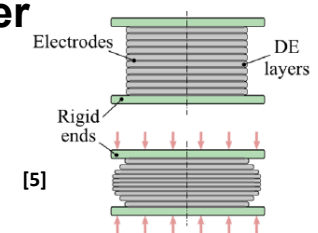
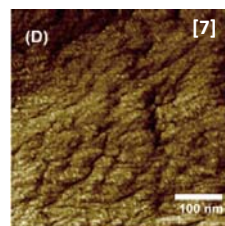
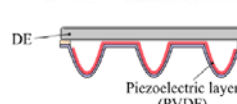
For smart watch (rotation)



For RFID tag (1-10 Hz)

## Dielectric elastomer & electrostrictive polymer

- Capacitance variation is obtained with a **deformable elastomeric dielectric material**
- Polarization:
  - ✓ Include an electret or a piezoelectric layer
  - ✓ Or need an external electric field
- Upto  $\text{mJ} / \text{cm}^3$  / mech. cycle @ few kV and few 100's% of deformation
- **Applications:** high-pressure/ extension systems (shoe sole, water waves...)

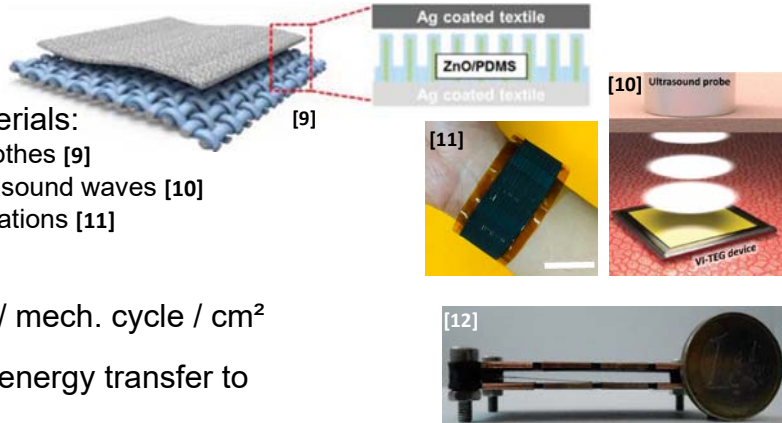


## Triboelectric (nano)generators (TENG)

→ Polarization by contact electrification of an electret layer

→ Mostly direct-pressure devices

- Lot of possible flexible materials:
  - ✓ Harvesting from textile in clothes [9]
  - ✓ Power transfer through ultrasound waves [10]
  - ✓ Patches for "2<sup>nd</sup> skin" applications [11]
  - ✓ Air flow harvesting [12]
- Typ. tens of nJ to a few  $\mu\text{J}$  / mech. cycle /  $\text{cm}^2$
- **Applications:** smart textile, energy transfer to implant...



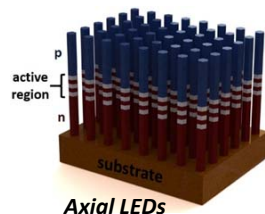
## Nanowire Light Emitting Diodes

Maria Tchernycheva (C2N)

- Integration of Thermoelectric micro-nano devices
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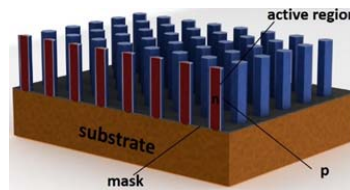
## Potential benefits of nanowire-based LEDs

- Efficient strain relaxation – **dislocation-free nanocrystals**, higher QE
- Growth on **low-cost substrates**



**Axial LEDs**

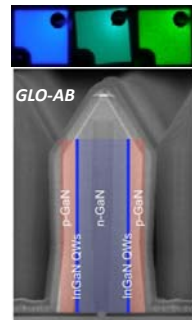
[1]



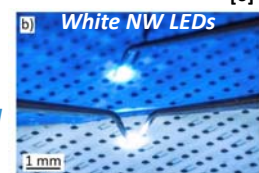
**Radial LEDs**

- ✓ Dislocation free highly mismatched active region (e.g. In-rich InGaN/GaN QDiscs to cover the **green gap**)
- ✓ Increase of the emitting surface - decrease of current density – **reduction of droop**
- ✓ Lateral surface is non-polar – **no field**
- Strong effort on NW LEDs to compete with planar LEDs
- Small size – mechanical flexibility. Good for alternative applications : **flexible light sources**

**Core/shell InGaN NW LED**



[6]



## Flexible nanowire LEDs

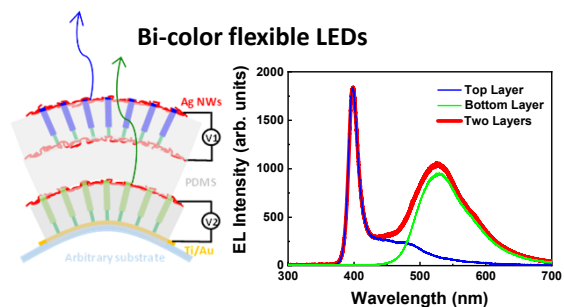
### Flexible blue, green and white light sources and high brightness displays using nanowire/polymer membranes

- Combine crystalline III-V **materials** with flexible polymers
  - ✓ Flexibility of polymers and high efficiency and long lifetime of crystalline materials
  - ✓ Modularity – combination of “incompatible” materials

**Flexible blue, green and white NW LEDs**



**Bi-color flexible LEDs**



# Adiabatic logic with micro/nano devices

Gaël Pillonnet (CEA)

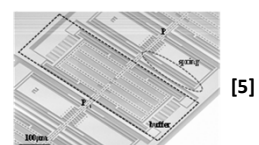
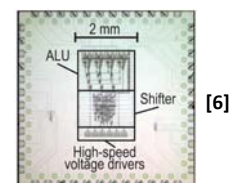
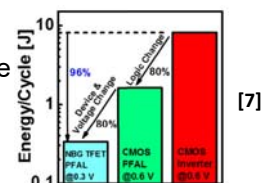
- Integration of Thermoelectric micro-nano devices
- Photovoltaics devices
- Thermophotovoltaic devices (with nanoscale gaps)
- Piezoelectric Kinetic Energy Harvesters
- Electrostatic Kinetic Energy Harvesters
- Nano LED
- Adiabatic logic with micro/nano devices
- Piezoelectric energy converters
- Metrology for thermal properties of thermoelectric micro/nano devices
- Metrology & Standardization for kinetic energy harvesters

Working Group 6 : Micro Nano Devices



## Devices for near zero-power Boolean operations

- **Fundamental limitation of FET approaches [1]**
  - ✓ Ultimate power consumption ( $\sim kBT$ ) is not achievable using nano-scale transistors [2,9]
- **Adiabatic Operations**
  - Reduce current flows and recycle energy by reducing the speed [3]
    - ➔ Enable near zero-power operations from energy-reversible micro-scale devices
- **Candidate to be a « Landauer switch » :**
  - ✓ MEMS w/ or w/o mechanical/electrical contact [4,5,8]
  - ✓ Solid state devices: Josephson [6], tunnel junction [7]
- **Applications**
  - ✓ Processing in IoT devices
    - + fundamental : explore the minimal energy floor of Boolean operations



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# Piezoelectric energy converters

Gaël Pillonnet (CEA)  
Dejan Vasic (SATIE)

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## Piezo Resonators for Power Converters

### • Motivation:

- 1/ Magnetic devices have a sub-linear scaling-law in miniaturization [1,2]
  - 2/ Piezoelectric resonators are inductive behavior and can be scaled down without penalty [3]
- Revival piezo MEMS devices for power conversion

### • Ideal candidate [4]:

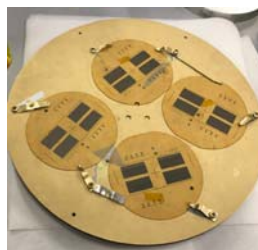
- ✓ high coupling ( $k^2 > 10\%$ ),
- ✓ high quality ( $Q > 1000$ ),
- ✓ MHz frequency ( $\gg$  harvesting)

### • State of art:

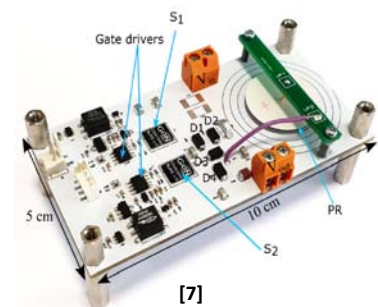
- ✓ macro-scale PZT [5],
- ✓ LnO MEMS [6]

### • Applications

- ✓ DC-DC converters
- ✓ 1's Watt @MHz with  $\text{cm}^2$ -scale devices could be achieved



[6]



[7]

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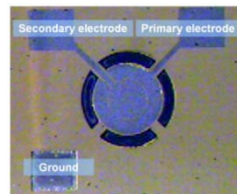
## Micro Transformers for Power Converters

### • Motivations:

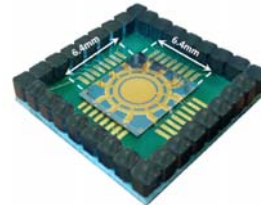
- 1/ Need to supply micro-systems @high voltage and @low-power
- 2/ Find a alternative to bulky magnetic transformers with poor scale-down property
- 3/ Piezo Devices is almost a planar devices
  - ➔ Piezo devices are a key enabler to achieve low form factor and scale down the size vs power requirements

### • SoA candidate:

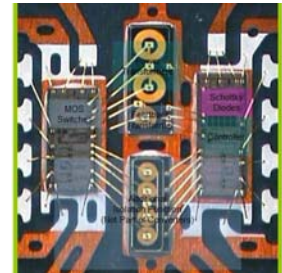
- ✓ PZT transformer [8],
- ✓ AlN [9],
- ✓ Coreless [10]



[8]



[9]



[10]

### • Applications:

- ✓ DC-DC converter from  $\mu\text{W}$  to mW, MHz, 100's V

## Metrology for thermal properties of thermoelectric micro/nano devices

Nolwenn Fleurence (LNE)

- Integration of Thermoelectric micro-nano devices
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## What is metrology and why is it useful ?

Reliability, reproducibility and comparability

physical value      number      unity

$$Q = (\{Q\} \pm \{u\}) [Q]$$

uncertainty



**Challenge:** Measurement techniques at macroscale not suitable for nanoscales!

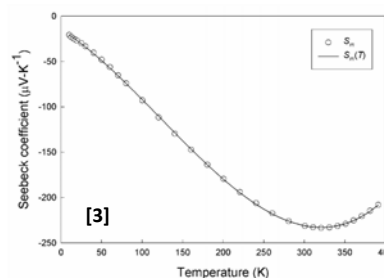
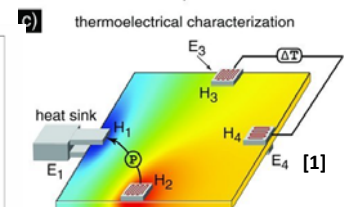
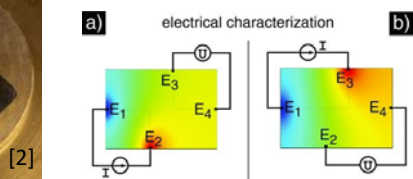
## Need: characterise thermoelectric conversion efficiency

- Dimensionless figure of merit:

$$ZT = \frac{S^2 \cdot \sigma}{k} T$$

➡ Suitable for material characterization

Combined van der Pauw Approach  
Uncertainty: 5 to 10% [2]



- Certified materials:

VAMAS TWA 38 Thermoelectric Materials

✓ SRM® 3451

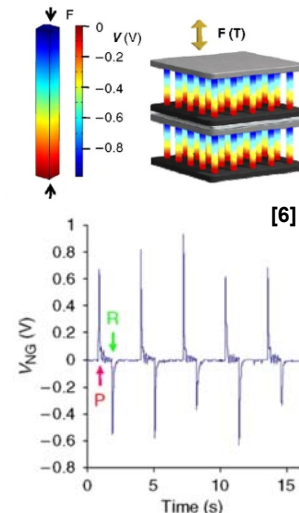
✓ SRM® 3452



## Metrology – Standardization: Mechanical EH

### Characteristics of nanodevices

- Current works use various "standards" for performance evaluation
- Trend to “oversell” the performances of (nano)materials & devices:
  - ✓ **Peak Power** (instead of average power)
  - Or even worse:
  - ✓ **Power** =  $V_{oc\_peak} \times I_{sc\_peak}$
- **Need for common definitions, unified methods and protocols**



## Metrology – Standardization: Mechanical EH

### International Standard: IEC 62830

<https://www.iec.ch/> [1]



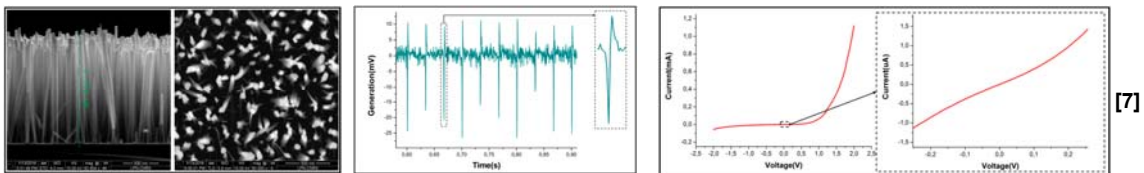
International  
Electrotechnical  
Commission

- Part-1: Vibration based **piezoelectric** energy harvesting
- Part-2: Thermo power based **thermoelectric** energy harvesting
- Part-3: Vibration based **electromagnetic** energy harvesting
- Part-4: Test and evaluation methods for **flexible piezoelectric** energy harvesting devices
- Part-5: Test method for measuring generated power from **flexible thermoelectric** devices
- Part-6: Test and evaluation methods for **vertical contact mode triboelectric** energy harvesting devices
- Part-7: Linear **sliding mode triboelectric** energy harvesting

## Metrology – Standardization: Mechanical EH

### International Standard for Nanomaterials?

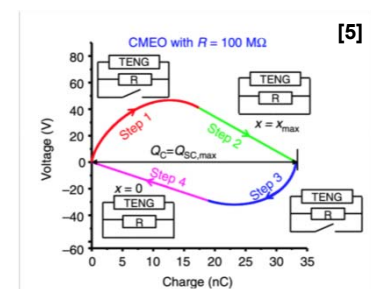
- **Thin films:**
- IEC 62830-2: **thermoelectric thin film** EH devices
- IEC 62047-30: MEMS **piezoelectric thin film** EH devices
- **Standardized Metrology for Piezoelectric Nanowires? Not yet**



## Metrology – Standardization : Mechanical EH

### Need for “fair” characterization methods for nanomaterials/nanodevices used by all

- At the material level
  - figures of merit (Priya [2])
  - energy density  $\text{J/m}^3$ ,  $\text{J/kg}$ ... and test conditions
- At the device/system level
  - figures of merit (Mitcheson [3], Badel [4], Zi [5])
  - energy per mechanical cycle, per  $\text{m}^3$  of "real volume"...



## Metrology – Standardization : Mechanical EH

- Differentiating between

- ✓ Inertial
  - Need a feedback control, at least for frequency sweep
  - Frequency-sweep back-and forth
  - Definition of bandwidth
- ✓ Normal excitation
  - Need a force sensor
- ✓ Direct friction
  - How evaluate the mechanical input?

- Electrostatic KEH

- ✓ Normalization from QV-max for given  $C_{\max}$ ,  $C_{\min}$  &  $V_{\text{bias}}$
- ✓ Breakdown voltage
- ✓ DC output voltage

- Output power

- ✓ in “steady-state” (at optimum operating point)
- ✓ or from scratch (all cap/induct discharged)?

## Conclusion

- Presentation far from being exhaustive
- In particular, we are looking for contributions for electromagnetic kinetic energy harvesters
- Metrology could be a workshop by itself
- Please join us if you want to contribute

# Thank you for your attention!

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- Nolwenn Fleurence (LNE)

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- Dimitri Galayko (LIP6)
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- Jean-Paul Kleider (GEEPS)
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- Guylaine Poulin-Vittrant (GREMAN)
- Alain Sylvestre (G2ELAB)
- Dimitri Tainnof (MOIZ & Inst. Neel)
- Rodolphe Vaillon (IES)
- Dejan Vasic (SATIE)
- Katir Ziouche (IEMN)



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